

INVENTOR INFORMATION

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CORRESPONDENCE INFORMATION

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APPLICATION INFORMATION

Title Line One:: Method for Manufacturing a Semiconductor
Title Line Two:: Device
Total Drawing Sheets:: 7
Formal Drawings?: Yes
Application Type:: Utility
Docket Number:: P/2171-195
Secrecy Order in Parent Appl.?: No

CONTINUITY INFORMATION

This application is a:: DIVISION OF
> Application One:: 09/407,910
Filing Date:: 09-29-1999

PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 10-294459
Filing Date:: 10-01-1998
Country:: Japan
Priority Claimed:: Yes

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